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	Application No.	Applicant(s)	
Notice of Allowability	09/640,733	KISHIDA, MASAYA	
	Examiner	Art Unit	
	Derrick W. Ferris	2663	
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate comme GHTS. This application is	n this application. If not include unication will be mailed in due	ed course. THIS
1. This communication is responsive to <u>8/17/2000</u> .			
2. 🔀 The allowed claim(s) is/are <u>1</u> .			
3. The drawings filed on 17 August 2000 are accepted by the	Examiner.		
4.	been received. been received in Application cuments have been received of this communication to file ENT of this application. itted. Note the attached EX as reason(s) why the oath of the submitted. on's Patent Drawing Review as Amendment / Comment of the header according to 37 Ch sit of BIOLOGICAL MAT	on No In this national stage applicated in this national stage applicated a reply complying with the reconstruction of the drawings in the front (not the FR 1.121(d). ERIAL must be submitted. I	quirements IOTICE OF
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview S Paper No. 8), 7. ☐ Examiner's	nformal Patent Application (PTG summary (PTO-413), /Mail Date Amendment/Comment Statement of Reasons for Allo	ŕ

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DETAILED ACTION

Allowable Subject Matter

1. **Claim 1** is allowed.

The following is an examiner's statement of reasons for allowance:

2. Claim 1 is allowable over the prior art of record because the Examiner found neither prior art cited in its entirety, nor based on the prior art, found any motivation to combine any of the said prior art references which teaches the specific structure with respect to interfacing analog and digital inversion circuits to both an A/D converter and an other A/D converter in order to solve the problem of compensating for distortion of the output signal due to the nonlinearity of combing two A/D converters in addition to improving the S/N ratio.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Derrick W. Ferris whose telephone number is (571) 272-3123. The examiner can normally be reached on M-F 9 A.M. - 4:30 P.M. E.S.T.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chau Nguyen can be reached on (571) 272-3126. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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CHI PHAM

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600 9/27/67